## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Tu, et al.

Attorney Docket: TSM03-0426

Filed:

Herewith

Examiner:

**TBD** 

Serial No.:

**TBD** 

Art Unit:

**TBD** 

For:

MIM Capacitor Structure and Method of Manufacture

Mail Stop: Patent Application Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

## **INFORMATION DISCLOSURE STATEMENT**

The Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

Respectfully submitted,

October 20, 2003

Date

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INFORMATION DISCLOSURE				Application Number	TBD		
				Filing Date	Herewith		
STATEMENT BY APPLICANT			LICANT	First Named Inventor	Tu, et al.		
				Art Unit	TBD		
(use as many sheets as necessary)			cessary)	Examiner Name	TBD		
Sheet	1	of	1	Attorney Docket Number	TSM03-0426		

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where	
		Number - Kind Code <sup>2 (# known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear	
	1	US-6,051,462 A	04-18-2000	Ohno		
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	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,	T
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	T⁵
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OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	τ²	
	9	LIU, et al., Single Mask Metal-Insulator-Metal (MIM) Capacitor with Copper Damascene Metallization for Sub-0.18µm Mixed Mode Signal and System-on-a-Chip (SoC) Applications, 2000, Lucent Technologies Bell Laboratories, Murray Hill, NJ, US.		
	10	ISHIKAWA, et al., High-Capacitance Cu/Ta <sub>2</sub> O <sub>5</sub> /Cu MIM Structure for SoC Applications Featuring a Single-Mask Add-on Process, 2002, Central Research Laboratory, Kokubunji, Tokyo, Japan.		

Examiner	Date	 	
Signature	Considered		

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional). See Kinds Codes of USPTO Patent Documents at <a href="https://www.uspto.gov">www.uspto.gov</a> or MPEP 901.04. Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. Applicant is to place a check mark here if English language Translation is attached.

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